S57	1	"5869896".PN.	USPAT; USOCR	OR	OFF	2005/01/05 10:15
S56	1	"5729038".PN.	USPAT; USOCR	OR	OFF	2005/01/05 10:15
S55	1	("6627983").PN.	USPAT	OR	OFF	2005/01/05 10:15
S59	7	(("5481133") or ("5604377") or ("5915167") or ("5973392") or ("6087719") or ("6133640") or ("6239495")).PN.	USPAT	OR	OFF	2005/01/05 10:28
S61	109	438/74.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/05 10:59
S60	701	438/64,66,67.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/05 10:59
S63	16263	sensor adj1 array	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/20 14:11
S62	2	programmable adj1 sensor adj1 array	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/20 14:11
S66	344	S65 and processing adj1 units	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/20 14:24
S65	8631	logic adj1 blocks	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/20 14:24
S64	37	S63 and logic adj1 blocks	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/20 14:24

S69	735	S68 and sensor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/20 14:29
S68	6303	FPGAs	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/20 14:29
S67	88	S66 and sensor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/20 14:29
S71	897	configurable adj1 logic adj1 blocks	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/20 14:35
S70	28	S69 and logic adj1 blocks	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/20 14:35
S73	22	S71 and sensor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/20 14:36
S72	1	S71 and sensor adj1 array	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/20 14:36
S1	1	("6355501").PN.	USPAT	OR	OFF	2005/05/26 09:02
S74	650	configurable adj1 logic adj1 blocks	USPAT	OR	OFF	2005/05/26 09:06
S75	14	S74 and sensor	USPAT	OR	OFF	2005/05/26 09:08
S77	7	configurable adj1 logic adj1 block and sensor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/26 09:09

S76	2371	logic adj1 block and sensor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/26 09:09
S79	2371	sensor and logic adj1 block	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/26 09:11
S78	17	configurable adj1 logic adj1 block and image	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/26 09:11
S81	16321	sensor adj1 array	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/26 09:30
S80	395	S79 and pixel	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/26 09:30
S82	105	S81 and logic adj1 block$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/26 09:33
S84	303	S81 and fpga$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/26 09:34
S83	0	S81 and fpga@1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/26 09:34

S87	587	S86 and logic adj1 device	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/26 09:47
S86	899	configurable adj1 logic adj1 blocks	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/26 09:47
S88	319	S87 and logic near2 circuit	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/26 09:48
S85	10803	S81 and field adj1 programmable adi1 gate adj1 arrays	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/26 09:53
S89	975	S85 and pixel	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/26 09:59
S91	1	("6462365").PN.	USPAT	OR	OFF	2005/05/26 10:02
S90	258	S81 and FPGA	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/26 10:02
L1	3681	257/72,258,290-293.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/26 14:18
L2	2308	257/443-448.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/26 14:21

L3	2234	257/685-686.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/26 14:25
L4	4330	257/687-691.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/26 14:28
L5	4092	257/692-696.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/26 14:29
L6	1873	438/109,113,74.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/26 14:42
L7	2176	438/64,66,67,460,462.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/26 14:49
L9	7	programmable adj1 sensor and logic adj1 circuit	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/26 15:02
L8	0	programmable adj1 sensor and configurable adj1 logic adj1 circuit	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/26 15:02

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	2	(SENSOR ADJ1 ARRAY AND LOGIC ADJ1 BLOCK).CLM.	US-PGPUB	OR	OFF	2005/10/26 15:20
L2	0	(SENSOR ADJ1 ELEMENT AND LOGIC ADJ1 BLOCK).CLM.	US-PGPUB	OR	OFF	2005/10/26 15:20
L3	1	(SENSOR ADJ1 ARRAY AND PROGRAMMABLE ADJ1 MODULE). CLM.	US-PGPUB	OR	OFF	2005/10/26 15:21